

02/21/02

10/069306
13 Rec'd PTO 21 FEB 2002
Sheet 1 of 1

Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-303US1	Application No.
		Applicant Gilbert Wolrich et al.	
		Filing Date February 21, 2002	Group Art Unit

Information Disclosure Statement
by Applicant
(Use several sheets if necessary)

(37 CFR §1.98(b))

U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
u	AA	5,220,669	06/15/1993	Baum et al.			
u	AB	5,428,779	06/27/1995	Allegrucci et al.			
u	AC	5,926,646	07/20/1999	Pickett et al.			
u	AD	6,061,711	05/09/2000	Song et al.			
u	AE	6,101,599	08/08/2000	Wright et al.			
u	AF	6,115,777	09/05/2000	Zahir et al.			
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449

U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-303US1Application No.
10/069,306**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

Applicant
Gilbert Wolrich et al.Filing Date
February 21, 2002

Group Art Unit

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
h	AA	WO 01/50679	07/12/2001	WIPO				
h	AB	WO 01/50247	07/12/2001	WIPO				
h	AC	WO 01/48619	07/05/2001	WIPO				
h	AD	WO 01/48606	07/05/2001	WIPO				
h	AE	WO 01/48599	07/05/2001	WIPO				
h	AF	WO 01/48596	07/05/2001	WIPO				
h	AG	WO 01/41530	06/14/2001	WIPO				
h	AH	WO 01/16782	03/08/2001	WIPO				
h	AI	WO 01/16770	03/08/2001	WIPO				
h	AJ	WO 01/16769	03/08/2001	WIPO				
h	AK	WO 01/16718	03/08/2001	WIPO				
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h	AV	Byrd et al., "Multithread Processor Architectures," <i>IEEE Spectrum</i> , Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
h	AW	Chang et al., "A New Mechanism For Improving Branch Predictor Performance," IEEE, pp. 22-31 (1994).

Examiner Signature

Date Considered

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Substitute Form PTO-1449 (Rev. 10-1-99)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-303US1	Application No. 10/069,306
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 21, 2002	Group Art Unit

Other Documents (include Author, Title, Date, and Place of Publication)

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	AX	Doyle et al., <i>Microsoft Press Computer Dictionary</i> , 2 nd ed., Microsoft Press, Redmond, Washington, USA, 1994, p. 326.
	AY	Farkas et al., "The multicluster architecture: reducing cycle time through partitioning," IEEE, vol. 30, December 1997, pp. 149-159.
	AZ	Fillo et al., "The M-Machine Multicomputer," IEEE Proceedings of MICRO-28, 1995, pp. 146-156.
	AAA	Gomez et al., "Efficient Multithreaded User-Space Transport for Network Computing: Design and Test of the TRAP Protocol," <i>Journal of Parallel and Distributed Computing</i> , Academic Press, Duluth, Minnesota, USA, vol. 40, no. 1, 10 January 1997, pp. 103-117.
	ABB	Haug et al., "Reconfigurable hardware as shared resource for parallel threads," IEEE Symposium on FPGAs for Custom Computing Machines, 1998.
	ACC	Hauser et al., "Garp: a MIPS processor with a reconfigurable coprocessor," Proceedings of the 5 th Annual IEEE Symposium on Field-Programmable Custom Computing Machines, 1997.
	ADD	Hennessy et al., "Computer Organization and Design: The Hardware/Software Interface," Morgan Kaufman Publishers, pp. 476-482 (1998).
	AEE	Hyde, R., "Overview of Memory Management," <i>Byte</i> , vol. 13, no. 4, 1998, pp. 219-225.
	AFF	Intel, "IA-64 Application Developer's Architecture Guide," Rev.1.0, May 1999, pp. 2-2, 4-29 to 4-31, 7-116 to 7-118 and c-21.
	AGG	Keckler et al., "Exploiting fine grain thread level parallelism on the MIT multi-ALU processor," IEEE, June 1998.
	AHH	Litch et al., "StrongARMing Portable Communications," IEEE Micro, 1998, pp. 48-55.
	AII	Mendelson et al., "Design Alternatives of Multithreaded Architecture," <i>International Journal of Parallel Programming</i> , vol. 27, no. 3, Plenum Press, New York, USA, June 1999, pp. 161-193.
	AJJ	Paver et al., "Register Locking in Asynchronous Processor Computer Design: VLSI Processors," ICCD '92 Proceedings, IEEE 1992 International Conference, 1992, pp. 385-398.
	AKK	Schmidt et al., "The Performance of Alternative Threading Architectures for Parallel Communication Subsystems," Internet Document, <i>Online!</i> , 13 November 1998.
	ALL	Thistle et al., "A Processor Architecture for Horizon," IEEE, 1998, pp. 35-41.
	AMM	Tremblay et al., "A Three Dimensional Register File for Superscalar Processors," IEEE Proceedings of the 28 th Annual Hawaii International Conference on System Sciences, 1995, pp. 191-201.
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	AOO	Turner et al., "Design of a High Performance Active Router," Internet Document, <i>Online</i> , 18 March 1999.
	APP	Vibhatavanijt et al., "Simultaneous Multithreading-Based Routers," Proceedings of the 2000 International Conference of Parallel Processing, Toronto, Ontario, Canada, 21-24 August 2000, pp. 362-359.
	AQQ	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
	ARR	Waldspurger et al., "Register Relocation: Flexible Contents for Multithreading," Proceedings of the 20 th Annual International Symposium on Computer Architecture, 1993, pp. 120-130.

Examiner Signature 	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

Substitute Form PTO-1449 Modified	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-303US1	Application No. 10/069,306
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.85(b))		Applicant Gilbert Wolrich et al.	
		Filing Date February 21, 2002	Group Art Unit

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
4	ASS	Wazlowski et al., "PRSIM-II computer and architecture," IEEE Proceedings, Workshop on FPGAs for Custom Computing Machines, 1993.

Examiner Signature	Date Considered
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR
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Application Number : 10/069306
 Confirmation Number: 1999
 First Named Applicant: Gilbert Wolrich
 Attorney Docket Number: 10559-303US1



Handwritten signature/initials

Art Unit:

Examiner:

Search string: (6223238 or 6223279 or 6247025 or 6256713 or 6272616 or 6275505 or 6279113 or 6289011 or 6298370 or 6307789 or 6324624 or 6345334 or 6347344 or 6351808 or 6356962 or 6373848 or 6360262 or 6389449 or 6393483 or 6415338 or 6426940 or 6427196 or 6430626 or 6434145 or 6463072 or 6532509 or 6552826 or 6560667 or 6577542 or 6584522 or 6587906 or 6606704 or 6625654 or 6629237 or 6631430 or 6631462 or 6661794 or 6667920 or 6668317 or 6681300 or 6694380 or 4023023 or 5113516 or 5363448 or 5436626 or 5600812 or 5652583 or 5717760).pn

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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<i>h</i>	2	6223279	2001-04-24	Nishimura et al.			
<i>h</i>	3	6247025	2001-06-12	Bacon			
<i>h</i>	4	6256713	2001-07-03	Audityan et al.			
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<i>h</i>	10	6307789	2001-10-23	Wolrich et al.			
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Signature

Examiner Name	Date
	

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

12/24/04

Title of Invention

INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR

Application Number : 10/069306
Confirmation Number: 1999
First Named Applicant: Gilbert Wolrich
Attorney Docket Number: 10559-303US1



Art Unit:

Examiner:

Search string: (5568617 or 5574922 or 5574939 or 5592622 or 5606676 or 5613071 or 5613136 or 5623489 or 5627829 or 5630130 or 5644623 or 5649157 or 5659722 or 5680641 or 5689566 or 5692167 or 5699537 or 5717898 or 5721870 or 5742587 or 5742782 or 5742822 or 5745913 or 5751987 or 5761507 or 5761522 or 5781774 or 5784649 or 5784712 or 5796413 or 5797043 or 5809235 or 5809530 or 5812839 or 5812868 or 5828746 or 5828863 or 5832215 or 5835755 or 5854922 or 5860158 or 5886992 or 5887134 or 5890208 or 5892979 or 5905876 or 5905889 or 5915123 or 5933627 or 5659687).pn

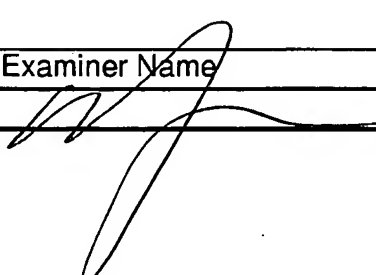
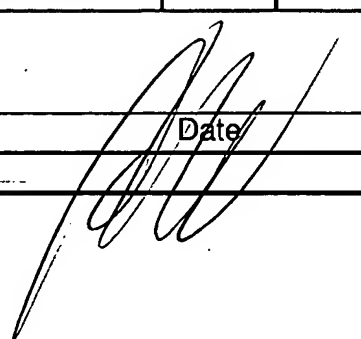
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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
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2	2	5574922	1996-11-12	James			
3	3	5574939	1996-11-12	Keckler et al.			
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Signature

Examiner Name	Date
	

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

12/07/04

Title of Invention

INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR

Application Number : 10/069306
Confirmation Number: 1999
First Named Applicant: Gilbert Wolrich
Attorney Docket Number: 10559-303US1
Art Unit:



Examiner:

Search string: (5937187 or 5938736 or 5940612 or 5940866 or 5946487 or 5948081 or 5951679 or 5958031 or 5961628 or 5970013 or 5978838 or 5983274 or 5996068 or 6002881 or 6009515 or 6012151 or 6014729 or 6023742 or 6029228 or 6058168 or 6058465 or 6067585 or 6070231 or 6072781 or 6073215 or 6076158 or 6079008 or 6079014 or 6085215 or 6085294 or 6092127 or 6092158 or 6092175 or 6112016 or 6115811 or 6134665 or 6141689 or 6141765 or 6144669 or 6145054 or 6157955 or 6160562 or 6182177 or 6195676 or 6199133 or 6201807 or 6212542 or 6212611 or 6216220 or 6223207).pn


US Patent Documents

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
W	1	5937187	1999-08-10	Kosche et al.			
W	2	5938736	1999-08-17	Muller et al.			
W	3	5940612	1999-08-17	Brady et al.			
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Signature

Examiner Name	Date
	

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

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12/54/04

Title of Invention	INSTRUCTION FOR MULTITHREADED PARALLEL PROCESSOR
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Application Number : 10/069306
 Confirmation Number: 1999
 First Named Applicant: Gilbert Wolrich
 Attorney Docket Number: 10559-303US1



Art Unit:

Examiner:

Search string: (3373408 or 3478322 or 3792441 or 3913074 or 3940745 or 4130890 or 4392758 or 4400770 or 4514807 or 4523272 or 4724521 or 4745544 or 4777587 or 4866664 or 5073864 or 5140685 or 5142683 or 5155831 or 5155854 or 5168555 or 5173897 or 5255239 or 5263169 or 5347648 or 5357617 or 5367678 or 5390329 or 5392391 or 5392411 or 5392412 or 5404464 or 5404482 or 5428809 or 5432918 or 5442756 or 5448702 or 5450351 or 5452437 or 5459842 or 5463625 or 5467452 or 5481683 or 5487159 or 5517628 or 5517648 or 5542070 or 5542088 or 5544236 or 5550816 or 5557766 or 20020053017 or 20020056037 or 20030041228 or 20030145159 or 20030191866 or 20040039895 or 20040054880 or 20040071152 or 20040073728 or 20040073778 or 20040098496 or 20040109369 or 20040205747).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<i>h</i>	1	3373408	1968-03-12	Ling			
<i>h</i>	2	3478322	1969-11-11	Evans			
<i>h</i>	3	3792441	1974-02-12	Wymore et al.			
<i>h</i>	4	3913074	1975-10-14	Homberg et al.			
<i>h</i>	5	3940745	1976-02-24	Sajeva			
<i>h</i>	6	4130890	1978-12-19	Adam			
<i>h</i>	7	4392758	1983-07-12	Bowles et al.			
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<i>h</i>	9	4514807	1985-04-30	Nogi			
<i>h</i>	10	4523272	1985-06-11	Fukunaga et al.			
<i>h</i>	11	4724521	1988-02-09	Carron et al.			
<i>h</i>	12	4745544	1988-05-17	Renner et al.			
<i>h</i>	13	4777587	1988-10-11	Case et al.			
<i>h</i>	14	4866664	1989-09-12	Burkhardt Jr. et al.			
<i>h</i>	15	5073864	1991-12-17	Methvin et al.			

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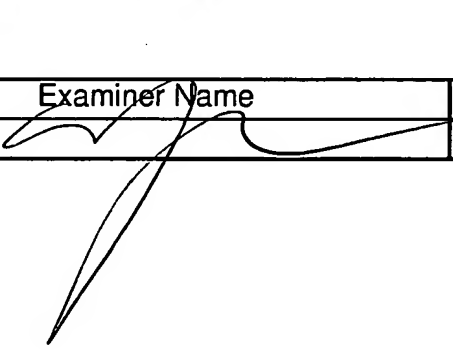
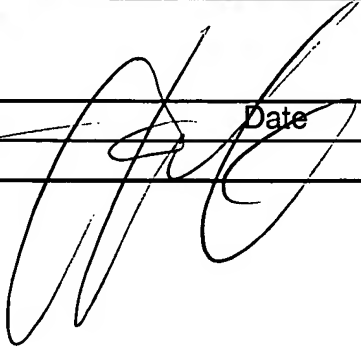
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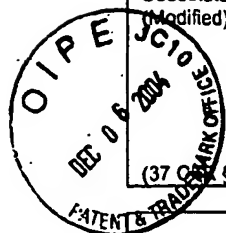
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